

**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Aptina Imaging CorporationApplication No./Patent No.: 7,002,628 Filed/Issue Date: February 21, 2006Titled: ANALOG TO DIGITAL CONVERTER WITH INTERNAL DATA STORAGE

Aptina Imaging Corporation, a Corporation  
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest in;
  2. ☐ an assignee of less than the entire right, title, and interest in  
 (The extent (by percentage) of its ownership interest is \_\_\_\_\_ %); or
  3. ☐ an assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)
- the patent application/patent identified above by virtue of either:

- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

OR

- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

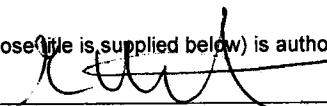
1. From: Panicacci, Roger To: Photobit Corporation  
 The document was recorded in the United States Patent and Trademark Office at  
 Reel 009877, Frame 0289, or for which a copy thereof is attached.
2. From: Photobit Corporation To: Micron Technology, Inc.  
 The document was recorded in the United States Patent and Trademark Office at  
 Reel 012745, Frame 0385, or for which a copy thereof is attached.
3. From: Micron Technology, Inc. To: Aptina Imaging Corporation  
 The document was recorded in the United States Patent and Trademark Office at  
 Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet(s).

- ☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

  
 \_\_\_\_\_  
 Signature

Gianni Minutoli  
 Printed or Typed Name

February 23, 2009  
 \_\_\_\_\_  
 Date

Attorney for Assignee  
 \_\_\_\_\_  
 Title

## PATENT ASSIGNMENT AGREEMENT

### 1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(1)

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

[the rest of this page intentionally left blank]

Agreed to by:

**Micron Technology, Inc.**

Signed: [Signature]

Date: 9/26/08, 2008

**Notarization**

State of Idaho

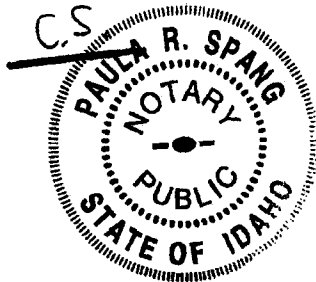
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]  
My commission expires: 8/12/11

REVIEWED  
MTI LEGAL



Agreed to by:

**Aptina Imaging Corporation**

Signed: Thomas J. Laws Jr.

Date: 9/26/08, 2008

**Notarization**

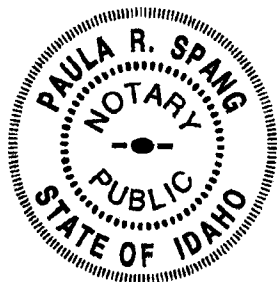
State of Idaho

County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas J. Laws, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]  
My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

**ATTACHMENT "A"**

**IMAGING PATENTS**

## Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
6995911	2002-1310.00/US	TIR CMOS IMAGER MICRO LENSES AND METHOD FOR INCREASING AREA COVERAGE AND CONTROLLING SHAPE OF MICRO LENSES	United States of America	Feb 7, 2006
7001785	2002-1006.00/US	TOTAL INTERNAL REFLECTION CMOS IMAGER	United States of America	Feb 21, 2006
7002231	2004-0067.00/US	BARIER REGIONS FOR IMAGE SENSORS	United States of America	Feb 21, 2006
7002628	PBIT-0037.00/US	ANALOG TO DIGITAL CONVERTER WITH INTERNAL DATA STORAGE	United States of America	Feb 21, 2006
7005628	PBIT-0146.00/US	AMPLIFIER WITH VARIABLE SIGNAL GAIN AND MATCHED GAIN BANDWIDTH	United States of America	Feb 28, 2006
7008816	2003-0184.01/US	IMAGER PHOTO DIODE CAPACITOR STRUCTURE WITH REDUCED PROCESS VARIATION SENSITIVITY	United States of America	Mar 7, 2006
7009227	2002-1143.00/US	PHOTODIODE STRUCTURE AND IMAGE PIXEL STRUCTURE	United States of America	Mar 7, 2006
7009546	PBIT-0020.00/US	Three-sided buttable CMOS Image Chip	United States of America	Mar 7, 2006
7012557	2004-0332.00/US	RAMP MODULATION ADC FOR IMAGERS	United States of America	Mar 14, 2006
7012645	PBIT-0095.00/US	IMAGE SENSOR WITH P-TYPE CIRCUITRY AND N-TYPE PHOTOSENSOR	United States of America	Mar 14, 2006
7012754	2004-0213.00/US	APPARATUS AND METHOD FOR MANUFACTURING TILTED MICROLENSES	United States of America	Mar 14, 2006
7013044	PBIT-0129.01/US	IMAGE SENSING SYSTEM WITH HISTOGRAM MODIFICATION	United States of America	Mar 14, 2006
7015448	2002-0308.00/US	DARK CURRENT REDUCTION CIRCUITRY FOR CMOS ACTIVE PIXEL SENSORS	United States of America	Mar 21, 2006
7015844	2003-0820.00/US	MINIMIZED SAR-TYPE COLUMN-WIDE ADC FOR IMAGE SENSORS	United States of America	Mar 21, 2006
7015981	AVGO-0046.00/US	DIGITAL IMAGE SYSTEM AND METHOD FOR COMBINING DEMOSAICING AND BAD PIXEL	United States of America	Mar 21, 2006